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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/944,426	08/30/2001	Vladislav Vashchenko	75292/13356	1844
7590 01/07/2005			EXAMINER	
Jorgen K Vollrath 588 Sutter Street #531 San Francisco, CA 94102			NADAV, ORI	
			ART UNIT	PAPER NUMBER
			2811	

DATE MAILED: 01/07/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.		Applicant(s)	
	09/944,426		VASHCHENKO, VLADISLAV	
	Examiner		Art Unit	
	ori nadav		2811	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 06 December 2004.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-4 and 6 is/are pending in the application.
- 4a) Of the above claim(s) 1 is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 2-4 and 6 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Claim Objections

Claim 2 is objected to because of the following informalities: The phrase "a n+ region" in line 3 should read "an n+ region".

Appropriate correction is required.

Claim Rejections - 35 USC § 112

The following is a quotation of the first paragraph of 35 U.S.C. 112:

The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention.

Claims 2-4 and 6 are rejected under 35 U.S.C. 112, first paragraph, as failing to comply with the written description requirement. The claim(s) contains subject matter which was not described in the specification in such a way as to reasonably convey to one skilled in the relevant art that the inventor(s), at the time the application was filed, had possession of the claimed invention. There is no support for the claimed limitations of forming at least one p+ region and at least one additional n+ region to define at least one p-n junction between the at least one p+ region and the at least one additional n+ region in the p well, as recited in claim 2, since the at least one p+ region and the at least one additional n+ region in the p well are separated by an isolation region.

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There is no support in the disclosure for the claimed limitations of at least one highly doped n+ region and at least one highly doped p+ region formed in the p-material of the p-well being forward biased relative to each other during normal operation, as recited in claim 3.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 2-4 and 6 are rejected under 35 U.S.C. 102(e) as being anticipated by Ker et al. (6,465,848).

Regarding claim 2, Ker et al. teach in figure 3a and related text a method of increasing the holding voltage of a LVTSCR structure that includes an n-well 42 and a p-well 44 formed in a substrate 40, an n+ region 46 and a p+ region 48 formed in the n-well 42, and an n+ region 54 formed in the p-well 44, the method comprising forming at least one p+ region 58 and at least one additional n+ region 60 inside the p-well 44 of the structure to define at least one p-n junction between the at least one p+ region 58 and

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the at least one additional n+ region in the p well that is forward biased during normal operation.

Regarding claims 3-4 and 6, Ker et al. teach in figure 3a and related text a method of increasing the holding voltage of a LVTSCR structure having an anode in an n-well 42 and a cathode in a p- well 44, comprising providing an alternative current path from anode to cathode through the p-well of the structure, other than purely the current path from anode to cathode through at least one highly doped n+ region and at least one highly doped p+ region formed in the p-material of the p-well, the at least one highly doped n+ region and at least one highly doped p+ region formed in the p-material of the p-well being forward biased relative to each other during normal operation, wherein the alternative current path defines a lower resistance current path than the p-well, and wherein at least one diode is formed in the p-well which provides a low resistance current path through the at least one diode once the threshold voltage across the at least one diode is exceeded.

Response to Arguments

Applicant argues that Ker does not teach at least one highly doped n+ region and at least one highly doped p+ region formed in the p-well are forward biased, because the n+ region and the p+ region of Ker et al.'s device are connected to a common contact.

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There is no support in the disclosure for the claimed limitations of at least one highly doped n+ region and at least one highly doped p+ region formed in the p-material of the p-well being forward biased relative to each other during normal operation, as recited in claim 3. Furthermore, Ker et al. teach in figure 3a and related text a current path in the direction from the anode to the n+ region in the p-well. Therefore, the n+ region and the p+ region formed in the p-well are forward biased, as claimed.

Papers related to this application may be submitted to Technology center (TC) 2800 by facsimile transmission. Papers should be faxed to TC 2800 via the TC 2800 Fax center located in Crystal Plaza 4, room 4-C23. The faxing of such papers must conform with the notice published in the Official Gazette, 1096 OG 30 (November 15, 1989). The Group 2811 Fax Center number is (703) 308-7722 and 308-7724. The Group 2811 Fax Center is to be used only for papers related to Group 2811 applications.

Any inquiry concerning this communication or any earlier communication from the Examiner should be directed to *Examiner Nadav* whose telephone number is **(571) 272-1660**. The Examiner is in the Office generally between the hours of 7 AM to 4 PM (Eastern Standard Time) Monday through Friday.

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Any inquiry of a general nature or relating to the status of this application should be directed to the **Technology Center Receptionists** whose telephone number is **308-0956**.

A handwritten signature in black ink, appearing to read 'Ori Nadav', with a stylized, sweeping flourish at the end.

O.N.
January 3, 2005

ORI NADAV
PRIMARY EXAMINER
TECHNOLOGY CENTER 2800